Electronic Acknowledgement Receipt					
EFS ID:	6672354				
Application Number:	10518779				
International Application Number:					
Confirmation Number:	6609				
Title of Invention:	Insulated-gate semiconductor device and approach involving junction-induced intermediate region				
First Named Inventor/Applicant Name:	Kailash Gopalakrishnan				
Customer Number:	40581				
Filer:	Robert J. Crawford/Kelly Waltigney				
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		STFD035US_RCE.pdf	853552	yes	16
·	'		f23fb626837d8a544d61dbe14473e28b28c 19890		
	Multip	part Description/PDF files in	.zip description		
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	Request for Continued Examination (RCE)		1	2	
	Claims		3	12	
	Applicant Arguments/Remarks Made in an Amendment		13	16	
Warnings:					
Information:					
2 Fee Worksheet (PTO-875)	fee-info.pdf	30587	no	2	
		6aca782eef668c6f8418898452c80afaf93ef e4d			
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#### National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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